



CR1 = 1N4001, optional, in case of signal reversal

LM10 = NSC LM10CLN or LM10CLH amp/reference

IC2 = 4N28 or similar, optoisolator

V2 = Normally low; high signifies fault ($I < 3.7$ mA)

V3 = Normally high; low signifies fault ($I < 3.7$ mA) (buffered output)

*C1 = 1 μ F optional, to avoid false output when large AC current is superimposed on 4.0 mA. Disconnect this capacitor when using with circuit of Figure 2.

\triangle = 1/6 MM74C04 or similar, CMOS inverter

FIGURE 1. Current Loop Fault Detector

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